



INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known			
		Application Number	09/921377		
		Filing Date	08/02/01		
		First Named Inventor	G. Michael Uhler		
		Group Art Unit	2183		
		Examiner Name	Eric Coleman		
Sheet	1	of	1	Attorney Docket Number	MIPS.0110-00-US

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
EC	AA	RISC Microprocessor Databook, 32-Bit TX System RISC TX39 Family TX39/H2 Processor Core, Chapters 1-8, Appendix A, Doc. ID 4412D-9908 (1998)	
EC	AB	Toshiba Data sheet List; http://www.seicon.toshiba.co.jp/eng/prd/micro/td/td_all.html ; Toshiba Semiconductor Company.	
EC	AC	Alpha Architecture Handbook (October 1996). pp 11-2 and C-20.	
EC	AD	CompactRISC CR16A Programmer's Reference Manual (February 1997); pp 2-6, 3-3, 5-12, and 5-13.	

Examiner Signature		Date Considered	6/14/05
--------------------	--	-----------------	---------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.